

**MULTI-LAYER CLASS IDENTIFYING COMMUNICATION APPARATUS  
WITH PRIORITY CONTROL**

**Background of the Invention**

5    1. Field of the Invention

The present invention relates to a multi-layer class identifying communication apparatus used in an IP network of a network layer of an OSI reference model.

10   2. Description of the Related Art

Recently, the Internet, which is regarded as a de facto standard, has been standardized as a global network, based on Transmission Control Protocol/Internet Protocol (TCP/IP). Usually, in the  
15   seven layers of the OSI reference model, the IP serves as the network layer, and the TCP serves as the transport layer. Data is passed from the lowest layer Ethernet and token-ring network to the IP and passed from the TCP to the application layer.

20        A router as a relay device between local-area networks (LAN) is arranged between a repeater, a bridge, and a gateway. The router is used to accumulate frames sent from a communication line and transmit them to an appropriate communication line  
25   according to a network address included in the data of the frames.

A conventional router fairly handles all IP

09752520-010304  
T02070-02525250

In contrast, the known ATM technology introduces the concept of connection, in which a route connecting a source address and a destination address is clearly defined by a virtual path identifier/a virtual channel identifier (VPI/VCI). This concept permits the quality of service (QOS) such as delay characteristics and discard ratios, which is necessary for a connection unit to be defined. In addition, network apparatuses perform priority controls so that the QOS of connections are satisfied.

Consideration is now given to technologies for easily performing priority control on the Internet. Typical examples are Intserve/Resource Reservation Setup Protocol (RSVP) as a protocol for performing the network band control, and Differentiated Service. The

former simulates the concept of connection as introduced in the ATM, while the latter performs priority control based on packet data as much as possible.

5           The Intserve/RSVP system is not applicable to a large-scale backbone network at low cost. That is, the system lacks scalability, and it is thereby not widely used. In order to solve the problem, the Differentiated Service primarily considers scalability,  
10 availability at low cost, and adaptability to the high-speed performance of an optical carrier (OC)-48 class as an interface for a backbone network optical fiber according to transmission velocities.

          The Differentiated Service guarantees  
15 quantitative service as in the ATM-QOS. Instead, from the viewpoint of the best effort, the Service sets a relative quality difference to facilitate the differentiation of service. This is accepted as a substantially practical solution judging from  
20 specification progress by the Internet engineering task force (IETF) as a Net-problem solving organization and vendor responses.

(Differentiated Service)

25           The Differentiated Service system has been discussed in the IETF, which regulates the Internet technologies. The Differentiated Service is a system

09753520-010304

(Service Classes in the Differentiated Service)

10           The Differentiated Service has three kinds of defined traffics (service classes) including an expedited forwarding service (EF class) as a premium service, an assured forwarding service (AF class), and a best effort service (BE class). The premium

15 expedited forwarding (EF) class provides a virtual dedicated-line service such as an IP-CBR (constant bit rate) on an IP network. Thus, it is necessary to perform precise transmission control including usage parameter control (UPC) by additionally using a

20 shaping section. Since the EF class is regarded as a class for a band guarantee service, it takes first priority over the assured forwarding service (AF class) and the best effort service (BE class), which will be described below.

25           Unlike the expedited forwarding (EF) class as  
the premium class, the assured forwarding service (AF)  
class is only for a relative priority control

framework. The assured forwarding service has four kinds of delay classes and three kinds of discard classes. Delay priority control is performed by giving priority to an order in which IP packets in a network apparatus are transmitted. For example, this is effective in reducing the transmission delay of an application responsive to delay. Discard control is performed by giving priority to the discarding of packets in a place where congestion occurs in the network apparatus. The best effort service (BE class) is applied as a third traffic, besides the EF class and the AF class. Of the service classes, the lowest priority control is given to the BE class.

The above description outlines the Differentiated Service. However, the recommendation of the Differentiated Service is still in a state of flux. Thus, the definitions and usage described above can be changed.

In conjunction with the above description, a packet transmitting and receiving node is disclosed in Japanese Laid Open Patent application (JP-A-Heisei 2-87747). In this reference, the packet transmitting and receiving node is composed of a reception buffer, a header analyzing section, a reception packet storing section, a passage packet storage and analysis section, a generation packet storage and analysis section, a control section and a transmission switch. The

reception buffer stores reception packets. The header analyzing section analyzes a destination and a service class from a header of each of the reception packets stored in the reception buffer. The reception packet storage section stores the reception packets from the reception buffer when the reception packets are destined to the node. The passage packet storage section stores the reception packets from the reception buffer when the reception packets are destined to other nodes. The generation packet storage analysis section stores and analyzes packets to be sent from the node to other nodes for every service class. The control section is given the number of packets and a packet generation time as the analysis results of the passage packet storage and analysis section and the number of packets and a packet generation time as the analysis results of the generation packet storage and analysis section, when referring to a priority level predetermined for the passage packet storage and analysis section and the generation packet storage and analysis section to determine a transmission order of the packets from the storage and analysis sections, and determines whether the number of packets stored in each storage and analysis section exceeds a predetermined storage limitation packet count and whether the difference between the packet generation time and a present time

exceeds a predetermined delay limitation time for every storage and analysis section. The control section determines that the packet should be primarily sent with no relation to the priority level of the packet when the packet generation time or the number of packets exceeds the limitation. The transmission switch takes out the packet from the storage and analysis section to transmit it.

Also, a packet switching apparatus is disclosed in Japanese Laid Open Patent application (JP-A-Heisei 5-191455). In this reference, a routing operation to the whole packet switching network is carried out based on priority classes. A buffer is provided for every priority class, which is separately and independently allocated with a delay and a discard rate as a transmission quality. Packets stored in the buffer from a transmission line in relaying of packets are subjected to a classifying process of the packets. The number of packets after the classification is monitored and an estimation delay is calculated for every priority class. The packets with permissible long delays are set to have long routes for load distribution when load is rapidly increased. Packets with large discard rates are discarded. The packet switching apparatus is composed of line accommodating section and a control section. The line accommodating section has a routing table indicative of a route and

discard instruction for every priority class for the  
above operations. The control section updates the  
routing table for every priority class based on delay  
data from the line accommodating section and switching  
5 apparatus status data of other stations.

Also, a switching apparatus is disclosed in  
Japanese Laid Open Patent application (JP-A-Heisei 7-  
154421). In this reference, data packets for at least  
two different priority classes are stored in buffers,  
10 respectively. Each of the buffers is allocated with a  
threshold value. The threshold value for a higher  
priority class is equal to or higher than that for a  
lower priority class. When a packet newly arrives the  
buffer, the priority level of the packet and an  
15 occupation rate of the buffer are determined. The  
occupation rate is compared with the threshold value  
for the buffer and the packet is stored in the buffer  
or discarded based on the comparing result. At least  
threshold value is dynamically controlled.

20 Also, a cell transfer control method is  
disclosed in Japanese Laid Open Patent application  
(JP-A-Heisei 9-205441). In this reference, when a  
connection belong to a specific traffic class with no  
band reservation is established, a discard priority  
25 data notified from a generation unit is stored in a  
node unit in correspondence to an identifier of the  
connection. A cell belonging to the specific traffic

09753620.040304



class is selectively discarded based on a predetermined discard condition which is determined a congestion state and the priority level when the route for the connection is in the congestion state.

5           Also, a transmission traffic control apparatus is disclosed in Japanese Laid Open Patent application (JP-A-Heisei 10-23012). In this reference, a data is transmitted in a fixed length packet or cell. A transmission traffic control apparatus is connected  
10 to a virtual path (VP) switch network and is composed of a header converting section, a control section, a transmission scheduler, and a read control section. The header converting section allocates an output side VPI/VCI value, which has been determined upon the  
15 establishment of the connection, and a class identifier corresponding to an in-apparatus class to a reception cell. The control section classifies the reception cell based on the VPI value and the class identifier given to an in-apparatus header to write a  
20 class queue. There are a usual class buffer and a variable class buffer. The transmission scheduler controls the maximum rate for every VP and a rate of the variable queue. The read control section reads out data from the class queue in response to an  
25 instruction from the transmission scheduler. The variable buffer includes a forward resource managing cell (FRM cell) inserting section for inserting a FRM

0976520 040304

cell. A receiving side apparatus connected via the VP switch network is composed of an FRM cell extracting section for extracting the FRM cell which has been inserted, means for monitoring the content of EFCI  
5 based on the content of the reception cell for every VP, a EFCI register for holding a latest EFCI data, a function for stamping the content of the EFCI register on a congestion indication bit (CI) of a backward RM cell (BRM cell), and a BRM cell inserting section for  
10 inserting the BRM cell in a downstream. The transmission scheduler is composed of means for controlling the maximum rate for every VP, a control parameter, various parameters for controlling the variable class queue rate, and means for controlling  
15 the insertion of the FRM cell and the monitoring the reception of the BRM cell. Also, the transmission scheduler has a function to control a variable transmission rate in accordance with the reception of the RM cells and the congestion indication bit, and  
20 allocates a priority having a relation of (normal class) > (FRM cell) > (variable class) in a range to the maximum rate for every VP.

#### Summary of the Invention

25 Therefore, an object of the present invention is to provide a multi-layer class identifying communication apparatus flexibly adaptable to the

09752520-040304

function of a router connecting local-area networks by changing only parameters in accordance to service classes in a network layer of an ATM network handling IP packets.

5           In order to achieve an aspect of the present invention, a multi-layer class identifying communication apparatus, includes an input interface connected to input communication lines; a switch circuit; and an output interface connected to output  
10 communication lines. The input interface comprises an allocating section, which determines a class identifier indicative of one of classes to which an IP packet belongs, from header data of the IP packet received through one of the input communication lines,  
15 and allocates an IP-QOS (Internet-Protocol-Quality-of-Service) code to the IP packet. The switch circuit selects one of the output communication lines based on a destination address of the IP packet, such that the IP packet is outputted from the output interface to  
20 the selected output communication output interface.

Here, the allocating section specifies a priority traffic based on an optional combination of the IP header and values of a plurality of fields of a TCP header.

25           Also, the multi-layer class identifying communication apparatus may further include a scheduler controls the switch circuit for a scheduling

09752520-010304

5

10

15

20

25

Also, the output interface may include a payload memory; a FIFO memory; a transmission side switch interface which receives the IP packet from the input interface to store in the payload memory, and  
20 generates IP packet data to write in the FIFO memory; IP-QOS class scheduler which carries out a scheduling function and a queuing operation based on IP-QOS class code to primarily issue a transmission request such that the IP packet is transmitted with a priority; a  
25 transmitting section which transmits the IP packet transferred from the input interface to a data link layer and a network access layer; and a transmission

Also, the output interface may include a payload memory; a FIFO memory; a transmission side switch interface which receives the IP packet from the input interface to store in the payload memory, and  
20 generates IP packet data to write in the FIFO memory; IP-QOS class scheduler which carries out a scheduling function and a queuing operation based on IP-QOS class code to primarily issue a transmission request such that the IP packet is transmitted with a priority; a  
25 transmitting section which transmits the IP packet transferred from the input interface to a data link layer and a network access layer; and a transmission

side control section which control the transmitting section based on the priority. In this case, the scheduling function may be based on WRR (weighted round robin) method.

5           In another aspect of the present invention, a method of controlling a transmission of an IP packet flow, is attained by determining a class identifier indicative of one of classes to which an IP packet belongs, from header data of the IP packet received  
10 through one of input communication lines; by allocating an IP-QOS (Internet-Protocol-Quality-of-Service) code to the IP packet; and by selecting one of output communication lines based on a destination address of the IP packet, such that the IP packet is  
15 outputted from the output interface to the selected output communication output interface.

Here, the allocation may be attained by specifying a priority traffic based on an optional combination of the IP header and values of a plurality  
20 of fields of a TCP header.

Also, the method may further include scheduling transfer of the IP packet using one of a WRR (Weighted Round Robin Scheduling) method and a fixed priority scheduling method. In this case, the  
25 scheduling may be carried out based on the IP-QOS code.

Also, the method may further include managing a queue such that a plurality of IP packets can be

09752520-040304

Also, the method may further include monitoring traffic in units of the IP-QOS codes to restrict excessive traffic.

Also, the determination may be attained by  
0 extracting the header data and TCP header data of the  
IP packet; by referring to a class identifier memory  
to determine the class identifier, using the header  
data of the IP packet as a search key; by carrying out  
a priority control to the IP packet in which a  
5 destination has been specified, based on the IP-QOS  
code and IP packet data of the IP packet; and by  
transferring the IP packet in units of the class  
identifiers.

Also, the selection may be attained by

carrying out a scheduling function and a queuing operation to the IP packet based on IP-QOS class code such that the IP packet is transmitted with a priority; and by transmitting the IP packet transferred from the input interface to a data link layer and a network access layer based on the priority. In this case, the scheduling function may be based on WRR (weighted round robin) method.

10                    **Brief Description of the Drawings**

Fig. 1 is a block diagram showing a structure of a multi-layer class identifying communication apparatus according to the present invention;

15                    Fig. 2 is a diagram showing an operation of the multi-layer class identifying communication apparatus according to the present invention;

Fig. 3 is a diagram showing a main portion of an IP-QOS class determining section according to the present invention;

20                    Fig. 4 is a diagram showing an operation of a class search section according to the present invention;

Fig. 5 s a flow chart showing a process of the class search section according to the present invention;

25

Figs. 6A and 6B are a diagram showing a table structure of a CAM region division according to the



present invention and a diagram showing the structure of an IP Src Prefix entry storage region;

Figs. 7A and 7B are a diagram showing a table block structure of the IP Src Prefix entry storage region according to the present invention, and a diagram showing the structure of the IP Src Prefix entry storage region;

Fig. 8 is a diagram showing a table block structure of an IPINFO entry storage region according to the present invention;

Fig. 9 is a diagram showing a format of IPv4 & TCP/UDP/Other Header used in the present invention;

Fig. 10 is a principle diagram of an IP-QOS class scheduler according to the present invention;

Fig. 11 is a diagram showing a discarding control logic according to the present invention;

Fig. 12 is a flow chart showing a process of the scheduler according to the present invention;

Fig. 13 is a flow chart showing a process of the scheduler according to the present invention;

Fig. 14 is a diagram showing a main portion of a WRR object buffer according to the present invention;

Fig. 15 is a diagram showing policing and shaping in a token-bucket model according to the present invention; and

Fig. 16 is a diagram of the policing and

0975520-040304

shaping operation according to the present invention.

### Description of the Preferred Embodiments

Hereinafter, a multi-layer class identifying  
5 communication apparatus of the present invention will  
be described below in detail with reference to the  
attached drawings.

#### <The First Embodiment>

##### 10 (1) Structure

Fig. 1 is a block diagram showing the  
structure of a multi-layer class identifying  
communication apparatus according to the present  
invention. The communication apparatus is composed of  
15 input/output interface line cards, an  $N \times N$  cross-bar  
switch (1g), and a switch scheduler (1h).

The input interface line card is composed of  
an IP packet receiving section 1a, a reception-side  
switch interface control section 1b, a reception-side  
20 switch interface 1d, a reception-side payload memory  
1c, an IP-QOS class determining section 1e, and an IP-  
QOS code determination memory 1f. The output  
interface line card is composed of a transmission-side  
switch interface 1j, a transmission-side switch  
25 interface control section 1k, an IP-QOS class  
scheduler 1m, a class packet data queuing memory 1p,  
and an IP packet transmitting section 1q.

In this case, the input interface line card and the output interface line card are used. This is because card blocks having required members are attached to the cross-bar switch 1g switching from a source address to a destination address. However, these interface line cards may be simply provided as an input interface and an output interface. The multi-layer class identifying communication apparatus includes input/output interfaces and a switch scheduler 1h in addition to the cross-bar switch 1g. In addition, the communication apparatus may have a simpler structure including neither the cross-bar switch 1g nor the switch scheduler 1h that controls the switching of the cross-bar switch 1g. In this case, the functions and software programs in accordance with the embodiments of the present invention may be applied to a router handling IP packets on the Internet and supporting from physical layer to a network layer and a bridge having the functions of the physical layer and a data link layer and filtering the IP packets.

#### (Input Interface Line Card)

In the input interface line cards on the reception side, the IP packet receiving section 1a extracts IP packet header data and TCP/UDP header data contained in an upper layer than a layer for the IP

packet header data from packet data of each of the IP packets obtained by dividing a packet in a application layer based on TCP of a transport layer as layer 4 in the OSI reference model. The extracted data are  
5 transmitted to the IP-QOS class determining section 1e.

The IP packet receiving section 1a retrieves various kinds of conditions defined based on the contents of CAMs or a SRAM based on the received IP packet data from the IP packets. In addition, as  
10 actions satisfying the conditions, the IP packet receiving section 1a carries out a set of processes such as a queue priority control process, a filtering process (discarding/passing process), a SW priority/non-priority control process, and addition of  
15 a DSCP value of Differentiated Service.

The IP-QOS class determining section 1e accesses to the IP-QOS code (class identifier) determination memory 1f to acquire a class identifier, using as a retrieval key the header data of the  
20 received IP packet as a combination of the data of the layer 3 typified by the IP header and the data of the layer 4 typified by TCP/UDP headers, which is located above the layer 3. The IP-QOS class determining section 1e notifies the class identifier of the IP-QOS  
25 code to the reception-side switch interface control section 1b.

The IP-QOS class determining section 1e

monitors incoming traffic exceeding a transmittable capacity preset for each IP-QOS class. When the traffic exceeds the preset transmittable capacity, the IP-QOS class determining section 1e carries out

5 policing control such as discarding of packets having IP-QOS codes or reducing the transmission priority of the packets. With these functions, the IP-QOS class determining section 1e determines the transmission frequency of packets in accordance with a network

10 resource quantity allocated for the IP-QOS code of each of the packets.

The reception-side switch interface control section 1b carries out priority control to the cross-bar switch 1g by using the packet data received from

15 the IP-packet receiving section 1a and an IP-QOS class code corresponding to the packet and determined by the IP-QOS class determining section 1e.

The reception-side switch interface 1d controls sending requests for every class and every

20 output interface card. The reception-side switch interface 1d usually transmits a transmission request having a high priority to the switch scheduler 1h. This priority control corresponds to the reading of packets from the reception-side payload memory 1c to

25 the cross-bar switch 1g and called delay priority control. The reception-side switch interface 1d outputs the IP packets to the cross-bar switch 1g for

09/25/20 04:04:04

The reception-side payload data memory 1c stores the input IP packet data and empty packet data required when the IP packet is transmitted.

(Output Interface Line Card)

The FIFO memory 1n transmits packet header  
20 data from the transmission-side switch interface 1j in  
a first-in first-out manner while keeping a  
predetermined delay time.

The IP-QOS class scheduler 1m carries out queuing for each class based on IP-QOS class codes  
25 contained in objects. The IP-QOS class codes correspond with a plurality of delay classes and a plurality of discard classes. An object stored in a

queue having a high delay priority is preferentially transmitted to the transmission-side switch interface control section 1k. The IP-QOS class scheduler 1m has a scheduling function based on the Weighted Round Robin (WRR) system to control in such a manner that a required service quality can be provided. In addition, on the IP network, precise transmission control including usage parameter control (UPC) and a shaping section is carried out to a premium service class providing a virtual dedicated line.

The transmission-side switch interface control 1k outputs IP packets from the transmission-side switch interface 1j to the IP packet transmitting section 1q in the order of scheduling based on the priority of the IP-QOS class scheduler 1m.

The IP packet transmitting section 1q outputs the IP packets to the lower layers such as the data link layer and the network layer including the Ethernet and token-ring networks.

## (2) Functions

Fig. 2 illustrates the functions of the communication apparatus in accordance with the present invention. An example of the priority control applied in the present invention will be described using the structure of the communication apparatus shown in Fig. 1.

5

10

20

25



IP-QOS class unit scheduler, and carries out a process  
of outputting to the transmission-side switch  
interface control (1K) from the object of a high  
priority class EF > (AF1 to AF4/BE), for scheduling  
5 based on the IP-QOS class (in-apparatus QOS code)  
determined on the reception side. It should be noted  
that (AF1 to AF4/BE) carries out the scheduling based  
on the WRR system.

In the discard control by the IP-QOS class  
10 scheduler 1m, the scheduler 1m compares the thresholds  
of the three classes of H/M/L with a buffer length to  
carry out the discard control (the generation of a  
drop object) with respect to AF1 to AF4/BE. Regarding  
EF, the IP-QOS class scheduler 1m supports by adding  
15 one class of H.

In terms of additions to the EF class and a  
shaping function, s shaping (delay scheduling)  
function is carried out to a determined IP-QOS class  
(only the EF class). The shaping control is a token-  
20 bucket system equivalent to the policing control. In  
this system, the token length of each class is  
compared with the length of a packet to be transmitted.  
When the token length is shorter than the packet  
length, the transmission of the packet is postponed.

25 The IP-QOS class scheduler 1m uses the class  
object queuing memory SRAM 1p to carry out the  
scheduling of objects.

097E2520.010001

(Priority control)

As the switching priority control of the apparatus, the following four processes are assumed.

- 5 (1) Mapping into an in-apparatus delay class by the reception-side switch interface control section 1b

The mapping into the in-apparatus delay class is a method for transmitting a transmission request. In this method, designation of delay priority control  
10 for the two classes (H/L) leads six delay classes.

Before transmitting of a packet to an output IF line card on the transmission side, the reception-side switch interface 1d issues a connection request to the switch scheduler 1h. The switch scheduler 1h  
15 arbitrates transmission requests from input line cards and notifies the connection data of input/output paths to the cross-bar switch 1g. In addition, the scheduler 1h issues a connection arbitration resultant notice to each of the input line cards on the  
20 reception side.

The reception-side switch interface 1d controls the requests of each class and each output IF card. The reception-side switching interface 1d primarily transmits a high priority request to the  
25 switch scheduler 1h usually. The priority control corresponds to the reading of packets from the reception-side payload data memory 1c for the cross

bar switch 1g, and this is regarded as delay priority control.

In the case of mapping into the in-apparatus delay class, the reception-side switch interface control 1b positioned on a previous stage of the reception-side switch interface 1d determines the delay priority in accordance with the traffic classes of packets. In Fig. 2, in order to match with the number of classes compliant with the Differentiated Service, six kinds of delay priority classes are determined as the traffic classes of packets. In this figure, there is shown an example corresponding with the two kinds (High/Low) of class queues with respect to the cross-bar switch 1g.

15

(2) Mapping into a discard class carried out when an input packet is written in the reception-side payload memory 1c

In the mapping into the discard class, in order to acquire the address of an empty area (a free page address), the discard priority control of three kinds of classes (H/M/L) are designated. In this manner, the three kinds of discard classes correspond with the EF class.

25

When the input packet is written in the reception-side payload data memory 1c, the reception-side switch interface control section 1b positioned in

007E2520 010301

the previous stage of the payload data memory 1c acquires the address of the empty area (free page address) in the reception-side payload data memory 1c. With the use of the free page address, the input  
5 packet is written in the reception-side payload data memory 1c.

Concerning the acquisition of the empty-area address, the reception-side switch interface 1d has a few kinds of priorities. For simple description, Fig.  
10 2 shows an example in which three kinds of discard classes (H/M/L) are used.

The reception-side switch interface 1d monitors the capacity of the empty area of the reception-side payload data memory 1c. When the  
15 capacity is smaller than a predetermined high threshold, the reception-side switch interface 1d permits only the writing of a high priority packet. When the capacity is smaller than a predetermined low threshold, the reception-side switch interface 1d  
20 permits only the writings of an intermediate priority packet. Except for these cases, the reception-side switch interface 1d permits the packets of any classes to be written in the memory. This is discard control to the memory 1c. The discard control is brought into  
25 correspondence with the three kinds of discarded traffic classes.

The reading of a packet from the transmission  
5 payload data memory 1i is controlled by the IP-QOS  
class scheduler 1m, the transmission-side switch  
interface control 1k, and the transmission-side switch  
interface 1j.

15

20

20           The packet transmitted from the cross-bar  
switch 1g is written in the transmission-side payload  
memory. The data of the written packet is notified as  
objects to the IP-QOS class scheduler 1m. The IP-QOS  
class scheduler 1m controls queue lengths by classes  
25 in the transmission-side payload data memory. Thus,  
the IP-QOS class scheduler 1m compares the queue  
length with the threshold of the discard class to

determine whether packets contained in the transmission-side payload memory are discarded or not. As a result, this corresponds to the discard class of the traffic class.

5           The transmission-side switch interface control section 1k carries out packet transmission/packet discard by using two kinds (transmission/discarding) of objects. In the transmission of a packet, a packet residing in an FSU  
10 memory is read out and transmitted in response to a read command. In the case of discarding a packet, a packet residing in the FSU memory is discarded in response to a drop command.

15 (IP-QOS Class Determining section)

Fig. 3 illustrates the main part of the IP-QOS class determining section 1e. The details of the IP-QOS class determining section 1e will be described with reference to Fig. 3.

20           As shown in the figure, the IP-QOS class determining section 1e is composed of a header extracting section 3a, a header checking section 3b, an IP-QOS code search section 3c, a policing control section 3d, an IP-QOS code output section 3e, and a  
25 parameter register control section 3f.

The header extracting section 3a extracts predetermined data from the formats of the IP header

00763520-010394  
T00070-00000000

and TCP/UDP header of IPv4 shown in Fig. 9, and transmits field values included in the extracted data as IP data to the IP-QOS code search section 3c. In Fig. 9, a densely hatched section (Ver) shows a field  
5 to be checked. Also, roughly hatched sections (TOS, Src IP Address, Dst IP Address, L4 Src Port, and L4 Dst Port) show fields for specifying classes as objects of a search key.

The extracted data includes a 4-bit version  
10 (Ver), an 8-bit TOS (type of service) identifier, a source (Src) IP address, a destination (Dst) IP address, a L4 Src port number of a layer 4 header, and a L4 Dst port number of the layer 4 header. An Internet header length (IHL) indicates the size of the  
15 IP header, a datagram length indicates the total length of the entire packet including the IP header and IP data, and an identification indicates an identifier restoring a fragment. Also, a Flag M is composed of 3 bits, a 13-bit fragment offset indicates  
20 the location of a fragment after division in original data, and a time to live indicates a time during which the presence on a network is permitted. In addition, a protocol specifies the upper layer protocol, and a header checksum indicates the check sum of the IP  
25 header.

The IP header checking section 3b checks the normality of the IP header and outputs the result of

09753520-010004  
F000000000000000

5

10

15

20

25



5           The condition for passing an input packet is  
set to be "token    0" instead of "token    packet  
length".

The IP-QOS code output section 3e carries out the re-timing of an IP-QOS code determined by the IP-QOS code search section 3c, a filtering bit from the policing control section 3d, and error data from the header checking section 3b to output to the rear-stage reception-side switch interface control section 1b.

An operator determines how to combine fields included in the mapping (MF/BA classifier) IP headers of traffic classes and how to make the combined fields

correspond with the traffic classes. These issues are not specified in the recommendation of RFC of the IETF. In order to make the above correspondence with the traffic classes possible, it is necessary to carry out  
5 the mapping of traffic classes based on arbitrary combination of extracted header data.

For example, when priority control is carried out to specified IP traffic between certain contract users, the classification of traffic classes is  
10 carried out based on the combination of a source (Src) IP address and a destination (Dst) IP address. In the specified IP traffic, when priority control is carried out to only the traffic of HTTP (the protocol for exchanging hypertexts with a WWW server on the  
15 Internet), it is necessary to classify the traffic class by combining a Src Port number and a Dest Port number contained in the header of the upper layer. In addition, when priority control is carried out to only the traffic transmitted from a certain server, it is  
20 necessary to classify the traffic classes by referring to only an Src IP address as the IP address of the server and an Src Port number. As shown here, when the traffic classes are classified based on a combination the plurality of fields of the IP header  
25 and the upper layer, this method is called a multi-field (MF) classifier.

Other than the above method, there is a

09753520-010204

classifying method called a behavior aggregate (BA) classifier. The BA classifier classifies traffic classes by referring to only the TOS field of the IP header. The TOS field defined in the IP header is a special field defined for the Differentiated Service. The TOS field is used to reduce a procedure for classifying the traffic classes based on a combination of fields of the IP header in a router. That is, an upper-stage router classifies the traffic classes based on the fields of the IP header and adds the class data to the TOS field to be transmitted. Next-stage routers subsequent to the upper-stage router only need to carry out priority control to each traffic class by referring to only the TOS fields. Vendors, however, need to determine how to use the TOS fields. Thus, it is necessary to connect the apparatuses of the same affiliates (vendors) to each other or there are provided restrictions in that operational rules need to be established between adjacent routers. As shown above, mapping of the traffic class requires that registrations are permitted in accordance with various kinds of combinations of arbitrary fields. In the present invention, the MF classifier and the BA classifier can be simultaneously supported by the following method.

(Operation of the Class Search Section)

The operation of the class search section will be described with reference to Figs. 4 and 5.

Fig. 4 shows the structure and operation of the class search section 3c. Fig. 5 shows a searching operation flow. As shown in Fig. 4, the IP-QOS class search section 3c is composed of a layer 4 (L4) port converting SRAM, a source IP address search CAM, a destination IP address search CAM, a priority mapping search CAM, and a priority setting SRAM. With this structure, IP-QOS class search section 3c extracts software priority control data. In addition, the flow chart shown in Fig. 5 includes a CAM search section inputting the source IP address, the destination IP address, a TOS identifier, and a protocol, to output a searched address Q, and a SRAM access section inputting the searched address Q, the upper layer TCP source port, and the upper layer TCP destination port to output an IP-QOS code.

In this situation, the conditions data is mainly described on the CAM, and the action data is described on the SRAM. The actions described on the SRAM correspond with the conditions described on the CAM. Thus, disposing both CAM and SRAM as recording mediums is not necessarily a requirement. However, considering the high-speed operation of the SRAM, the CAM may be formed by dividing the memory area of the

0975530-040304

SRAM. In this case, it is primarily necessary to apply an implementation method for effectively using the resource (area: the number of entries) of the CAM.

As described above, the MF classifier

5 classifies traffic classes based on the arbitrary combinations of an Src IP address, a Dst IP address, an Src Port number, a Dst Port number, a protocol number, and a TOS. Regarding the MF classifier, the simplest searching method is a method in which the

10 values of a Src IP address, a Dst IP address, a Src Port number, a Dst Port number, a protocol number, and a TOS field are set as registered data on the CAM capable of designating a mask for each entry, and the searching operation is carried out based on the packet

15 header data every time a packet is input. However, since there are restrictions on the bit width of the CAM, practical ideas for implementation are needed.

The present invention has a sequence for acquiring an IP-QOS code by using the general-purpose

20 CAM and the following two-stage searching method. The BA classifier carries out the process of converting to an IP-QOS code by referring to only TOS fields in the same framework as the process carried out by the MF classifier. That is, the process by the BA classifier

25 can be regarded as a case in which only the TOS fields are usable in the case of the MF classifier.

Considering the configuration of the CAM, it is

09752520 040304

possible to operate using both classifiers.

First, as a prior processing, fields are degenerated into key values to be registered in the CAM and the SRAM. Next, as a classification process, 5 entries as the registered key values are searched on the CAM.

A detailed explanation will be given below with reference to Figs. 4 and 5.

#### 10 (Prior Registration in the CAM)

Prior Processing 1: the registration/degeneration of a Src IP address shown in step ① of Fig. 4

In advance, all Src IP addresses/prefixes 15 included in the entries for classification are registered. In the prior process, when the Src IP address and the Dst IP address are degenerated, the Src IP address is used as a search key to carry out the searching operation by use of the longest prefix 20 match under regulations based on the classless inter domain routing (CIDR). A CAM address obtained through the searching operation is set as the address A. When no key value is registered, all "0s" are a value indicating the address A. This process is equivalent 25 to step S1 shown in Fig. 5.

Prior Processing 2: the registration/degeneration of a Dst IP address shown in

09/25/2019 02:23:26

a step ② of Fig. 4

Similar to the prior processing 1, in advance, all Src IP addresses/prefixes included in the entries for classification are registered. The Dst address is used as a search key to carry out retrieval by the longest prefix match. A CAM address obtained by the searching operation is set as Addr\_B. When no key value is registered, all "0s" are the value of the Addr\_B. This process is equivalent to the process of step S2 shown in Fig. 5. When the CAMs are connected in parallel to each other, steps S1 and S2 shown in Fig. 5 can be carried out in parallel.

Prior Processing 3: the registration/degeneration of port numbers (the data of the layer 4 shown in steps ①' and ②' of Fig. 4. The purpose of the processing 3 is to classify the application of the layer 4 from the Src port number or the Dst port number to degenerate into a predetermined key value. The port numbers are classified into well-known port numbers defining the protocol of the layer 4 and numbers arbitrarily added by terminals. In the normal layer 4 application, the well-known port number of the layer 4 application executed by a server is added to the Dst port number of a packet routed to a server from a client. The well-known port number is registered in a memory (table). The well-known port numbers required in operation are limited to a few

09753220.040300

kinds of numbers (HTTP, TELNET, FTP, etc.). Thus, only a small amount of memory ( $256 \times 8 = 2064$  bit) is needed.

In the processing 3, the port number is used  
5 as the address and access is carried out to the memory for converting from the port number to a key value to acquire a predetermined key value. The key is composed of a code specifying the layer 4 and a flag designating whether the key value is valid for an Src  
10 port or a Dst port. The reason for designating the flags for the Src/Dst ports is that it is possible to classify a one-way traffic. For example, the class of traffic transmitted to a server can be treated as a high priority class, while the class of traffic  
15 transmitted to a client can be treated as the best effort class. As shown in steps S3 and S4 of Fig. 5, the processing is implemented on each of the Src port number and the Dst port number. The reading of a conversion key based on the Src port number is  
20 equivalent to the step S3 of Fig. 5. The reading of a conversion key based on the Dst port number is equivalent to the steps ①' and ②' of Fig. 4 and the step S4 of Fig. 5. The results of both processes mentioned above generates a key value into which a  
25 well-known port number is degenerated and a flag showing whether the key value comes from an Src port or a Dst port. This process is equivalent to the step

09752530-01000  
T000T00-02525260



S5 of Fig. 5 for calculating the Pory Key.

(Class determining process shown in steps ③ and ④ of Fig. 4)

5           The CAM is searched again based on key values, TOS fields, and protocol numbers obtained in the prior process shown in the steps S1 to S5 of Fig. 5. This process is equivalent to a step S6 of Fig. 5. The combination of keys, that is, keys to be used for the  
10   searching operation as valid data, are defined as mask data for each entry in the CAM. This process can provide a CAM address (addr\_Q) in which each entry is stored.

          Lastly, in a step S7 of Fig. 5, the CAM  
15   address (addr\_Q) is used as pointer data (address) for an external memory. As a result, ultimately required traffic class data can be obtained.

          The reception-side switch interface control section 1b maps data (IP\_INFO) obtained in the  
20   operation flow shown in Fig. 5 into an object and transmits to the reception-side switch interface 1d. Then, the input IF cards carry out a variety of priority control.

          It should be noted that when there is no hit  
25   in the registered data through the searching operation of the CAM, the CAM returns an address "0". This is defined as a class code indicating BE traffic. In the

007E2520-040304  
T02070-02525250

external memory, similarly, the value of the external memory at a reset time is set to be all "0s", and the area of the address "0" stores the data of BE traffic class. Thus, traffic which is not hit in the CAM

5 searching operation is regarded as BE traffic. The details of tables stored in the CAM are shown in Figs. 6A to 8.

Figs. 6A and 6B show structural examples of a table stored in the memory CAM (Contents Addressable  
10 Memory) and a table of a storage area for IP Src Prefix entries. Figs. 7A and 7b show a table of a storage area for IP Dst Prefix entries and a table of a storage area for IPINFO address entries. In addition, Fig. 8 shows a table of a storage area for  
15 IPINFO entries. In the cases of Fig. 6A to 8, reference character D indicates discard (0: normal, and 1: discard) in filtering. In addition, reference character P indicates packet priority (0: low priority, and 1: high priority); reference character route  
20 indicates an upper 1-bit route change flag (0: default route setting, and 1: lower 4-bit route field setting. Output TOS bit 9 indicates a DSCP update flag (0: non-rewriting of the TOS DSCP Field bit 7-2, and 1: implementation of rewriting thereof). Output TOS bit  
25 8 indicates a CU update flag (0: non-rewriting of TOS CU Field bit 1 and 0, and 1: implementation of rewriting thereof). Output TOS bit 7-2 indicates a

TOS DSCP (differentiated service code point) field.

Output TOS bit 1-0 indicates TOS CU (currently unused) Field.

5 (Traffic Regulating method)

As shown in Fig. 15, in the traffic regulating method of the present invention, traffic characteristics are expressed by a token-bucket model. The token-bucket model is applied in the policing  
10 section 3d of the reception-side interface section and the shaping section 8f of the transmission-side interface section.

That is, regarding algorism for detecting traffic violation, both shaping and policing section  
15 have an equal circuit structure. Fig. 15 shows the token-bucket model. The token-bucket model has a token-containing register (bucket b) to add tokens in the bucket cyclically. In this case, tokens increase at an average rate (r). In the token adding process,  
20 the value of (WC + token) is compared with the token upper limit value b, and when the value exceeds the upper limit value b, the value b is set.

As a condition for transmitting a packet, it is necessary that a token giving a size corresponding  
25 with the size of a packet to be transmitted is present in the bucket. After the packet is transmitted, the number of token giving a length equivalent to the

0974320 0252460  
T02040

transmitted packet length is reduced from the present number of token. In the packet transmission, when a token enough to give a length equal to or greater than the packet length is not present in the bucket, the  
5 transmitted packet is a traffic violation object.

When the condition is applied to the policing section 3d, the packet as the traffic violation object is discarded, or, for example, low priority marking is carried out. In the case of the shaping section 8f,  
10 after waiting until tokens enough to permit the transmission of the traffic violation packet are accumulated in the bucket, the packet is transmitted.

(Additional Description shown in Fig. 16)

15 The traffic violation occurs when there are not accumulated tokens enough to transmit a packet in the bucket. The number of tokens contained in the bucket increases at a ratio calculated by an expression:  $T \text{ (elapsed time)} \times r \text{ (average rate)}$ . When  
20 tokens accumulated in the bucket do not reach a required length even though a packet is received, the transmission of the packet is postponed until tokens reaching the required length are accumulated in the bucket.

25 Fig. 16 shows the operational images of policing and shaping. With reference to Fig. 16, a description will be given as follows.

0975520-010904

(Example of Policing Operation)

Assume that packet 1 arrives at time  $T_1$ . When this is not traffic violation pattern, the packet 1 is output at the same time  $T_1'$ . The number of tokens left in a bucket at the output time is indicated by  $X_1$ . It is supposed that packet 2 having size  $s_2$  arrives at time  $T_2$ . The number of tokens necessary to output the packet 2 is equivalent to  $S_2$ . However, it is supposed that the value of  $S_2$  is assumed to be greater than  $\{X_1 + (T_2 - T_1) \times r\}$ . In this case, a traffic violation is caused. As a result, the packet 2 is discarded.

(Example of Shaping Operation)

It is supposed that packet 1 arrives at time  $T_1$ . At this time, since a traffic violation is not caused, the packet 1 is output at the same time  $T_1'$ . The number of tokens left in the bucket at the output time is indicated by  $X_1$ .

It is supposed that packet 2 having size  $s_2$  arrives at time  $T_2$ . The number of tokens necessary to output the packet 2 is equivalent to  $s_2$ . However, it is supposed that  $S_2 > \{X_1 + (T_2 - T_1) \times r\}$ . In this case, a traffic violation is not caused. Thus, the packet 2 is output after waiting for a time  $\tau$  during which a required number of tokens are accumulated in the bucket. In this case, the time  $\tau$  can be calculated by an equation

09752520-040304

$$S2 = X1 + \{(T2 + \tau) - T1\} \times r.$$

(Details of Priority Control by Output Interface Cards)

5               With reference to Fig. 2, a description will  
be given of the priority control by the output  
interface (IF) cards. Regarding the output IF cards,  
an IP-QOS class scheduler 1m carries out predetermined  
priority control based on IP-QOS codes (traffic  
10 classes) defined in object data. The IP-QOS codes are  
defined as data for identifying traffic classes, and  
show a plurality of delay classes and a plurality of  
discard classes. The priority control carried out by  
the IP-QOS class scheduler 1m is equivalent to the  
15 steps ③ and ④ of Fig. 4. Fig. 10 shows a concept  
view of the priority control by the IP-QOS class  
scheduler 1m.

(Priority Control by the IP-QOS Class Scheduler)

20               The IP-QOS class scheduler 1m receives  
objects via a FIFO 1n from the transmission-side  
switch interface 1j and a classifier 8a classifies the  
objects. The classified objects are stored in a  
common buffer 8c selecting a queue for each class.  
25 The common buffer 8c stores the objects for each class.  
There are six kinds of object queues 8c in total  
allocated for the classes EF, AF1 to AF4, and BE.

0075520-040304  
REF ID: A2525260

These object queues are all controlled by the common buffer 8c. The definitions of the EF, AF, and BE classes have already been provided in the description of the conventional art.

5                Since the EF class is regarded as a band guarantee service, precise transmission control is carried out by additionally using a shaping section 8f. A Weighted Round Robin controller 8d shown in Fig. 10 carries out selector control to an output class selector 8e so that the EF class queue is primarily read before the AF/BE class queues are read. The AF and BE class queues will be described below. The shaping section 8f has a circuit structure composed of a token-bucket model equivalent to a policing unit.

10              The difference between a shaping section and a policing section exists only with regard to whether the transmission of the packet having a traffic violation is postponed or the packet is discarded. In this respect, as in the case of the policing section

15              in terms of implementation, transmission process can be carried out at high speed when the packet transmission is determined by using only bits added to tokens. Furthermore, the circuit structure can be simplified.

25              The scheduling of the AF and BE classes is carried out by the Weighted Round Robin (WRR) system.

09/03/00 09:00:00

(Additional Explanation of WRR)

The WRR scheduling shows an expanded round robin scheduling system. According to a predetermined weight ratio, service for each class is provided. In 5 the WRR system, each class has a counter. Each counter shows the number of cells (or packets having fixed lengths) routable by the time the counter is reset. At the reset time, the value of the counter is set to be the weight value of each class. When the 10 counter value of a selected class is equal to or greater than "0" and the buffer includes one or more cells, one cell of the class is output to decrement the counter value. When the weight values of all of the classes are "0" or the number of cells in the 15 buffer is "0", all of the weight counters are reset. Thus, when all classes have sufficient input traffics, the number of output cells corresponds with a weight ratio.

Each of the AF class and the BE class has a 20 weight counter 8g and a weight value. A Weighted Round Robin controller 8d determines the objects of the AF/BE classes to be read based on the weight values and the data of the queue lengths of class queues 8c of a common buffer to control the selector. 25 The queues of the selected AF class and BE class output the objects thereof in the order of FIFO.



(Discard control)

A discard control logic unit 8b shown in Fig. 10 carries out discard control based on a logic shown in Fig. 11. The queue length 8h of each class is controlled on the basis of input object data. A discard control threshold 8k is defined for each class. Additionally, the threshold of the entire common buffer 8c is defined. The discard control logic 8b carries out discard control by comparing the queue length of each class with the thresholds 8k of each class and notifies an object to be discarded as a drop object to a transmission-side switch interface unit 1j. In addition to the discard control carried out by the above class-unit-based comparison, another discard control is carried out by comparing the sum of the entire queue lengths of the same discard class with the thresholds thereof in the overall common memory 8c. In Fig. 11, there are provided three kinds of discard classes and three thresholds corresponding therewith. Specifically, there are four discard conditions, in which (1) no discard control is carried out over all of the classes; (2) discard control is carried out to only a low priority class; (3) discard control is carried out both over a low priority class and an intermediate priority class; and (4) discard control is carried out over all of the classes. In Fig. 11, when the value of the class queue length becomes equal

00753520 040304  
F0E0F0 0255260

to or larger than the value of threshold 3, regardless of whether there is an empty area or not, all packets are discarded.

Not only in the AF-class unit but also in the entire common memories, thresholds with respect to the sum of discard classes are similarly defined to carry out discard control.

(Buffer used for the WRR system)

Fig. 14 illustrates the main part of the buffer used for the WRR system. With respect to class object queues 8c, there are provided weight counters 1l1 to 1n1 controlling the present weight values and preliminary determination counters 1l2 to 1n2 controlling weight values after the weight counters are reset. When class determination cannot immediately be made in the first processing by the Weighted Round Robin controller, the similar class determination processing is made with the weight value of the preliminary determination weight counter obtained after the reset of the weight counter. Still, when this processing cannot make the class determination, fixed delay priority control is carried out to select the AF classes. The above class determination processings are carried out in parallel while excluding a loop processing. Thus, the processings can be carried out at high speed.

(Flow of Scheduling Operation)

Each of Figs. 12 and 13 shows the flowchart of the above-described scheduling. As processing paths, an EF class determination path and an AF/BE class determination path exist. The EF class has absolute precedence over the AF/BE classes. Thus, when there is a transmission request for an EF-class packet, the EF class packet is selected. Only when there is no transmission request for an EF-class packet, a predetermined AF/BE class is selected.

Regarding AF/BE class determination, the Weighted Round Robin system carries out first and second determinations, and fixed delay priority control is carried out to select from the AF/BE classes as a third determination.

When the processing flows shown in Figs. 12 and 13 are read, the following points need to be considered. In each of Figs. 12 and 13, there are shown parameters, which will be described as follows.

In the EF class, parameter  $W[EF]$  indicates an initial weight (added token value) of the EF class; parameter  $WH[EF]$  indicates an upper limit token value; parameter  $WC[EF]$  indicates a token value (variable) of the EF class; and parameter Add Time [EF] indicates a token addition cycle, which is, for example, set by an average rate under contract). In the AF/BE classes (AF1, 2, 3, and 4, and BE), parameter  $W[i]$  indicates a

class initial weight value; parameter WH[i] indicates  
a weight-counter upper limit value; parameter WC[i]  
indicates a weight counter value (variable); parameter  
WC[i]r indicates a weight counter value (variable)  
5 after a single reset of the weight counter; parameter  
WC[i]r WC[i] + \_ class ptr indicates a pointer  
showing a transmitted/searched class; parameter MPSZ  
(Maximum Packet Size) applied in the AF/BE classes  
indicates a packet maximum length used as a judgement  
10 reference for rare cases; and Q[i] > 0 indicates the  
absence or presence of an object in a queue. In  
addition, parameter fixed priority (Q[i]) indicates a  
situation in which an output request is transmitted in  
fixed priority order by AF1, AF1, AF2, AF3, AF4, and  
15 BE only based on the parameter Q[i] > 0 regardless of  
a WC value. Parameter Length indicates the length of  
a packet and is used as a common parameter among all  
of the classes.

First, the process of EF-class selection will  
20 be described below.

#### (Cyclic Addition of Token)

In Fig. 12, as an initial processing, in step  
S0a, a token WC[EF] is initialized to be "0". In step  
25 S0b, a token addition timing is generated. When it is  
a predetermined timing (cnt = = Add\_time [EF]), a  
token is added (step S0c). An initial token W[EF] is

09/5250-010301

5 When the former value exceeds the latter value, the latter value is set.

Next, as shown in Fig. 13, when an object  
10 reading request is received (step S1a), the queue  
length is determined (step S1b), and the presence or  
absence of a token is determined (step S1c).

Next, an explanation will be given of the AF/BE class selection processing.

The AF/BE class selection processing will be illustrated below with reference to Figs. 12 and 13.

(1) The values of both class pointer and weight counter are initialized (steps S0d and S0e).

(2) After an object reading request is received, only when the EF class object cannot be transmitted, that is, only when the results of the determinations obtained in steps S1b and S1d are both "No", the AF/BE class selection is carried out. In step S2b, the queue length is determined, and in step S2c, weight validity is determined.

(3) Since there is provided a plurality of AF/BE classes, the above determination processing (2) is carried out to all of the classes (step S2e). In step S2g, a class pointer (a pointer indicating a searched class) is updated in accordance with the order of Round Robin.

(4) When there is a class corresponding with sending conditions :queue length > 0 and weight value > 0, a sending request of the corresponding class (Send\_req [Class] = ON) is routed (step S2d).

The above first to fourth operations are equivalent to class selection by a first routine.

(5) In the above retrieving processing (3), when all of the classes do not correspond with the sending conditions, an initial weight value defined for each class is added to the present weight value in all of the classes. This is a weight-counter reset processing.

0975550-040304

(6) When class selection cannot be carried out in the first routine, that is, when there is no sending class candidate, class selection is carried out again by a weight value obtained after a single retransmitted of the weight counter reset in the processing (5). As shown in Fig. 11, there are a register (WC[class]) controlling a present weight value and, additionally, a register (WC[class]r) controlling a weight value after the single reset of the weight counter. As in the case of the processing (4), when there is a class corresponding with the sending conditions :queue length > 0 and weight value > 0, a sending request of the corresponding class (Send\_req [Class] = ON) is output.

The above operations (5) and (6) are equivalent to class selection by a second routine.

(7) In the second routine, when class selection cannot be carried out, that is, when there is no output class candidate, as an exceptional case, fixed priority control is carried out for class selection. The purpose of the priority control is to avoid a situation in which no packet cannot be routed although there is an empty space in the capacity of a line. A class to be output is selected based on a predetermined fixed priority. For example, in the case shown in Fig. 8, class selection is carried out in the priority order of AF1, AF2, AF3, AF4, and BE.

The processing does not depend on a weight value. In this case, an output candidate is determined only by determining whether an object is present or not in a queue. With this processing, two or more weight  
5 counter resets never happen in a single class selection processing. A normal determination is carried out by the Weighted Round Robin system. In addition, fixed delay priority (which is called a fixed priority mode in Fig. 13) is given to carry out  
10 an exceptional determination.

(8) An output class is selected by a sending request for the corresponding class selected in the operations (4), (6), and (7) (step S2n).

In the operation of step S2n, as a parameter,  
15 the maximum packet size MPSZ is used. This is because it is necessary to avoid a situation in which when a packet having an excessive length arrives, the value of the weight counter becomes a large negative value. For example, the maximum length of the IP packet is 64  
20 KB, which can be unusual as the size of a transmitted packet. Statistically, a usual packet length is assumed to be a few KB at largest. Thus, arrangement is made such that it is possible to set a maximum packet length regarded as an unusual packet length.  
25 In this arrangement, when a very large packet arrives, the arrived packet length is compared with the maximum packet length. When the arrived packet length is

09753530 040304



larger than the maximum packet length, it is regarded as an exceptional case, As a result, the reduction processing of a weight counter is skipped, or the weight value is forced to be "0" to impose a certain  
5 penalty.

According to the above embodiment, in the Differentiated Service whose specifications are still in flux, the TCP layer of the OSI reference model is compared with the IP layer thereof. From IP packets,  
10 IP-QOS codes are allocated independently from the IP packet. Preferably, various kinds of communication service qualities are classified based on the IP-QOS codes. The classification of the IP-QOS codes permits traffic congestion in communication systems to be  
15 relieved.

According to the present invention, the following advantages are attained.

(1) An operator can set prioritized traffic by combining the packet data of the layer 3 and that  
20 of the layer 4 (the flow unit of each upper application).

(2) Assuming versatile operation, Weighted Round Robin Scheduling (WRRS) can be combined with a fixed priority scheduling system. Each QOS class can  
25 be selected by the fixed priority scheduling, and a minimum frequency band can also be designated.

09752520-040304  
T02070 0252260